

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

I hereby certify that this transmittal of the below described document is being deposited with the United States Postal Service in an envelope bearing First Class Postage and addressed to the U.S. Patent and Trademark Office, Washington, D.C., 20231, on the below date of deposit.			
Date of Deposit:	02/10/03	Name of Person Making the Deposit:	KATHERINE RINALDI
		Signature of the Person Making the Deposit:	<i>Katherine Rinaldi</i>

In re Application of: Manfred Bartz, Marat Zhaksilikov, Steve Roe, Kenneth Y. Ogami, Matthew A. Pleis and Douglas H. Anderson

Serial No.: 09/989,570

Examiner: Dimyan, Magid Y.

Filed: 11/19/01

Art Unit: 2825

For: METHOD FOR FACILITATING MICROCONTROLLER PROGRAMMING

**Assistant Commissioner for Patents
Washington, D.C. 20231**

AMENDMENT TRANSMITTAL

1. Transmitted herewith is an amendment for this application

- ☒ Transmitted herewith is a response to an office action for the above identified patent application (26 sheets)
- ☒ Transmitted herewith are 6 sheets of Red Line drawings.
- ☒ Drawing Amendment 1 sheet
- ☒ Supporting Documents 4 sheets

2. Applicant is other than a small entity

Extension of Term

3. The proceedings herein are for a patent application and the provisions of 37 C.F.R. 1.136 apply.

- (a) [] Applicant petitions for an extension of time under 37 C.F.R. 1.136 (fees: 37 C.F.R. 1.17(a)-(d) for the total number of months checked below:)

<u>Extension</u>	<u>Fee</u>
[] one month	\$110.00
[] two months	\$410.00
[] three months	\$930.00
[] four months	\$1,450.00

Fee \$ _____

If an additional extension of time is required, please consider this a petition therefor.

- (b) [X] Applicant believes that no extension of term is required. However, this conditional petition is being made to provide for the possibility that applicant has inadvertently overlooked the need for a petition for extension of time.

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F Calculation

4. The fee for claims (37 C.F.R. 1.16(b)-(d)) has been calculated as shown below:

(for other than a small entity)					
Fee Items	Claims Remaining After Amendment	Highest Number of Claims Previously Paid For	Present Extra Claims	Fee Rate	Total
Total Claims	26	- 26 =	0	x \$18.00	\$0.00
Independent Claims	3	- 3 =	0	x \$84.00	\$0.00
Multiple Dependent Claim Fee (one or more, first added by this amendment)				\$260.00	\$0.00
Total Fees					\$0.00

PAYMENT OF FEES

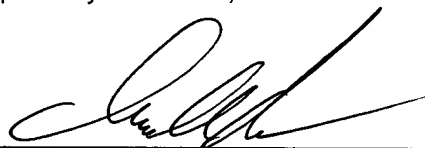
5. The full fee due in connection with this communication is provided as follows:
- ☒ [x] The Commissioner is hereby authorized to charge any additional fees associated with this communication or credit any overpayment to Deposit Account No.: 23-0085.
A duplicate copy of this authorization is enclosed.
- ☐ [] A check in the amount of \$
- ☐ [] Charge any fees required or credit any overpayments associated with this filing to Deposit Account No.: 23-0085.

Please direct all correspondence concerning the above-identified application to the following address:

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Two North Market Street, Third Floor
San Jose, California 95113
(408) 938-9060

Respectfully submitted,

Date: 2-10-2003

By: 
Anthony C. Murabito
Reg. No. 35,295



YPR-CD01167M

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a7red
2/26/03

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF:

Manfred BARTZ et al.

: GROUP ART UNIT:

SERIAL NO: 09/989,570

:

FILED: NOVEMBER 19, 2001

: EXAMINER:

FOR: METHOD FOR FACILITATING
MICROCONTROLLER
PROGRAMMING

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PRELIMINARY AMENDMENT

COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

SIR:

Prior to examination of the above-identified application, consideration of the following Amendments and Remarks is respectfully requested.

IN THE SPECIFICATION

Please replace the paragraph beginning at page 1, line 3 with the following new paragraph:

A1
The present invention relates to the field of programmable single-chip systems. Specifically, the present invention relates to a software program which facilitates design of a circuit having programmable system blocks by assisting in programming a microcontroller.

Please replace the paragraph beginning at page 4, line 12 with the following new paragraph:

A2
A method to facilitate circuit design is disclosed. In a method embodiment, in response to a selection of a module (e.g., amplifier, timer, PWM, etc.), information related to the selected module is displayed in a designing interface. For example, a schematic and data sheet for the selected module may be displayed. The module may be implementable in one or more digital and/or